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Patent Application

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Park et al. DOCKET: 8836-202 (IB12076-US)  
SERIAL NO: 10/635,253 GROUP ART UNIT: 2816  
FILED: August 6, 2003 EXAMINER: Nguyen, Minh T.  
FOR: **SEMICONDUCTOR INTEGRATED CIRCUIT COMPRISING  
FUNCTIONAL MODES**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Examiner:

In response to the Office Action dated July 8, 2004, Applicants provisionally elect the claims directed towards the Species I (claim nos. 1-7 and 10-15), with traverse. Claims 1-15 are pending.

As set forth in MPEP §803, if the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though the application contains claims to independent and distinct inventions.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 9, 2004.

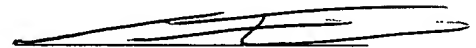
Dated: Aug. 9, 2004

  
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While the method of the first and second species may be distinct for the reasons set forth by the Examiner, Applicants believe that simultaneous examination will not present an undue burden. For example, the claims of the first and second species are drawn to "An integrated circuit device comprising a pin for receiving a direct current voltage component signal..." (see claims 1, 8 and 10). Applicants believe that no extraneous examination should result from any distinction between the claim groups.

Accordingly, withdrawal of the restriction requirement and examination of all claims is respectfully requested.

Respectfully submitted,



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